



PATENT
ATTORNEY DOCKET NO.: 040894-5699

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Takashi NAKAMURA et al.)	Confirmation No.: 1378
)	
Application No.: 09/935,639)	Group Art Unit: 2814
)	
Filed: August 24, 2001)	Examiner: S. Rao
)	
For: SEMICONDUCTOR DEVICE AND)	
METHOD FOR MANUFACTURING)	
THE SAME)	

Mail Stop ISSUE FEE

Commissioner for Patents
U.S. Patent and Trademark Office
2011 South Clark Place
Customer Window
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(i), Applicants bring to the attention of the Examiner the document listed on the attached PTO 1449. A copy of the listed document is attached.

Applicants respectfully request that this Information Disclosure Statement be considered or at least placed in the file.

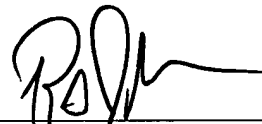
This submission does not represent that no better art exists and does not constitute an admission that the listed document is material or constitute "Prior Art." If it should be determined that the listed document does not constitute "Prior Art" under United States law,

Applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of such document. Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

While Applicants believe that no fee is due in connection with the filing of this paper, if there is any fee due, please charge the fee to our Deposit Account No. 50-0310.

Respectfully submitted,

MORGAN, LEWIS & BOCKIUS LLP

By: 
Robert J. Goodell, Ph.D.
Reg. No. 41,040

Dated: *March 17, 2004*

Customer No. 009629
MORGAN, LEWIS & BOCKIUS LLP
1111 Pennsylvania Avenue, N.W.
Washington, D.C. 20004
TEL: 202.739.3000
FAX: 202.739.3001

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Attorney Docket No. 040894-5699

Application No.: 09/935,639

Applicants: Takashi NAKAMURA et al.

PAGE 1 of 1

Filing Date: August 24, 2001

Group Art Unit: 2814

PTO Form 1449

U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date

FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class	Sub Class	<u>Translation</u> YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		"A Novel Stack Capacitor Cell for High Density FeRAM Compatible with CMOS Logic," by Takahisa HAYASHI et al., 2002 IEEE

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.